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** CONTINUING DATA *****

This application is a 371 of PCT/JP05/11606 06/24/2005

** FOREIGN APPLICATIONS *****

JAPAN 2004-195533 07/01/2004
JAPAN 2004-265753 09/13/2004

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Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <u>Examiner's Signature</u> <u>Initials</u>				

ADDRESS

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TITLE

Heterojunction bipolar transistor

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